The Delphion Integrated View

Title: **JP01152837A2: CONTROL CSMA PACKET SWITCHING SYSTEM**

Derwent Title: CSMA packet switch with random re-sensing points - uses relationship established between all-station and two-station throughputs, last throughput, and load factors [Derwent Record]

Country: **JP** Japan
Kind: **A**

High Resolution

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Published / Filed: **1989-06-15** / 1988-10-31

Application Number: **JP1988000273364**

IPC Code: IPC-7: **H04L 11/00;** H04L 11/00;

Priority Number: 1987-10-30 **GB1987000000254**

Abstract: PURPOSE: To hold optimum control by omitting an idle period in contact with a busy period for which the station performs transmission from a present evaluation value and performing the evaluation based on only a completely observed period.

CONSTITUTION: A controller 32 provided with a time counter 34, an evaluation device 36 and an updating device 38 is arranged in respective transmission/ reception stations. The evaluation device 36 respectively updates two variable SI and NI composed of the present sum of the number of the idle periods and an idle period length provided by a channel after the last updating of TS. The updating device 38 re-calculates the length of the next observation interval and an idle period correction value δ and finally resets the time counter E and the measured variables SI and NI to '0'. Thus, the minimum number of the idle periods capable of guaranteeing that the throughput of at least 90% of maximum possible throughput is to be achieved in the next period with the probability of 99% is selected.

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PDF	Patent	Pub.Date	Inventor	Assignee	Title
	US5428541	1995-06-27	Miyata; Hiroshi	Toyota Jidosha Kabushiki Kaisha	Throttle valve controller for engine system

Other Abstract Info: None